IN THE SPECIFICATION

Please replace paragraph **[0036]** with the following:

[0036] FIG. 3A shows a block diagram of an embodiment of a transmitter unit 300 that can perform transmit data processing for Channels 1 and 2 described above. Transmitter unit 300, which may be implemented within an access point or a user terminal, includes a transmit (TX) data processor 310, an optional TX spatial processor 330, and one OFDM modulator 340 for each transmit antenna 350.

Please replace paragraph **[0071]** with the following:

CE 9/21/09

0071

[~~1071~~] Coherent demodulation of each of the L data subbands is performed by first forming an estimate of the channel response for the data subband using the two pilot subbands flanking the data subband. The channel estimate $\hat{h}_i(k)$ for the k-th data subband may be obtained by combining the channel estimates for the two flanking pilot subbands, which may be expressed as:

$$\begin{aligned}\hat{h}_i(k) &= \hat{h}_i(k-1) + \hat{h}_i(k+1) \\ &= p_2^*(k-1)r_i(k-1) + p_2^*(k+1)r_i(k+1)\end{aligned} \quad \text{, for } k \in K_d \text{ and } i \in \{1 \ldots T\}, \text{ Eq (1)}$$

where ~~$p_2(k)$~~ $p_2^*(k)$ is the conjugate of the pilot symbol transmitted on the k-th subband for Channel 2 and K_d represents the set of data subbands, i.e., $K_d \in \{2, 4, \ldots 2L\}$.

Please replace paragraph **[0083]** with the following:

[0083] FIG. 8C shows a block diagram of a decision-directed detector 440e that performs coherent detection in the time domain and is another embodiment of detector 440c in FIG. 7. The samples $x_i(n)$ for each receive antenna are provided to a delay line/buffer ~~830~~ 831 that is implemented with $N-1$ delay units 832, each of which provides one sample period of delay.